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Amend A
R Smalls Log
1-15-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shigeo Nishitoba

Serial No.: 10/026,466

Group Art Unit: 2816

Filed: December 27, 2001

Examiner: Nguyen, Long T.

For: DRIVING CIRCUIT AND CONSTANT CURRENT DRIVING APPARATUS USING
THE SAME

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

TECHNOLOGY CENTER 2800
JAN - 8 2003

RECEIVED

AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

In response to the Office Action dated October 7, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATIONS:

Please amend paragraph 1, on page 21:

Fig. 7 is a circuit diagram showing the configuration of the driving circuit according to the second embodiment. In this driving circuit, a first current mirror circuit 10a is configured such that a PNP transistor Tr_x serving as the base current compensating circuit 21 is added to the first current mirror circuit 10 of the driving circuit according to the first embodiment.

Please amend paragraph 2, on page 21:

A base of the PNP transistor Tr_x 21 is connected to the collector of the PNP transistor Tr_o , an emitter is connected to the base of the PNP transistor Tr_o , and a collector is connected to a ground terminal 14.

Please amend paragraph 4, on page 21:

Also, a second current mirror circuit 20a is configured such that an NPN transistor Tr_y serving as a base current compensating circuit 22 is added to the second current mirror circuit